



# TECHNIQUES FOR HANDLING NOISE AND VARIABILITY IN ANALOG CIRCUITS

ON-LINE CLASS on MS TEAMS

January 15-26, 2024

<b>WEEK 1</b>	<b>JANUARY 15-19</b>			
<b>WEEK 2</b>	<b>JANUARY 22-26</b>			
<b>DAILY</b>	Central European Time	Eastern Standard Time	Pacific Standard Time	India Standard Time
	<b>CET (Lausanne)</b>	<b>EST (New York)</b>	<b>PST (California)</b>	<b>IST (India)</b>
Module 1	3:00-4:30 pm	9:00-10:30 am	6:00-7:30 am	7:30-9:00 pm
Module 2	5:00-6:30 pm	11:00 am-12:30 pm	8:00-9:30 am	9:30-11:00 pm
<b>WEEK 1</b>	Module			
DAY 1, Mon. January 15	1&2	Random Mismatch Origins		Marcel Pelgrom
DAY 2, Tue. January 16	1&2	Analyzing Mismatch and Yield in Analog Circuits		Marcel Pelgrom
DAY 3, Wed. January 17	1&2	Layout Strategies to Reduce Offset		Marcel Pelgrom
DAY 4, Thu. January 18	1&2	Fundamentals of Noise in Electronic Devices		Christian Enz
DAY 5, Fri. January 19	1	Offset and CMRR: Systematic and Random		Michiel Steyaert
	2	Voltage and Current References		Michiel Steyaert
<b>WEEK 2</b>	Module			
DAY 6, Mon. January 22	1	Noise Cancellation Techniques		Filip Tavernier
	2	Noise Sampling in Switched Capacitor Filters		Filip Tavernier
DAY 7, Tue, January 23	1&2	Noise Analysis in Continuous-Time and Sampled-Data Circuits		Christian Enz
DAY 8, Wed. January 24	1	Noise and Offset Reduction Techniques		Christian Enz
	2	Dynamic Offset-Cancellation Techniques		Kofi Makinwa
DAY 9, Thu. January 25	1	Dynamic Offset-Cancellation Techniques		Kofi Makinwa
	2	Dynamic Element Matching Techniques		Kofi Makinwa
DAY 10, Fri. January 26	1	Dynamic Element Matching Techniques		Kofi Makinwa
	2	Case Studies in Precision Analog Circuit Design		Kofi Makinwa