TECHNIQUES FOR HANDLING NOISE AND VARIABILITY IN ANALOG CIRCUITS

ON-LINE CLASS on MS TEAMS
January 16-27, 2023

WEEK 1	JANUARY 16-20
WEEK 2	JANUARY 23-27

DAILY	Central European Time	Eastern Standard Time	Pacific Standard Time	India Standard Time
	CET (Lausanne)	EST (New York)	PST (California)	IST (India)
Module 1	3:00-4:30 pm	9:00-10:30 am	6:00-7:30 am	7:30-9:00 pm
Module 2	5:00-6:30 pm	11:00 am-12:30 pm	8:00-9:30 am	9:30-11:00 pm

WEEK 1	Module		
Monday, January 16	1&2	Random Mismatch Origins	Marcel Pelgrom
Tuesday, January 17	1&2	Analyzing Mismatch and Yield in Analog Circuits	Marcel Pelgrom
Wednesday, January 18	1&2	Layout Strategies to Reduce Offset	Marcel Pelgrom
Thursday, January 19	1&2	Fundamentals of Noise in Electronic Devices	Christian Enz
Friday, January 20	1	Offset, CMRR and PSRR	Willy Sansen
	2	Variability in Bandgaps	Willy Sansen

WEEK 2	Module		
Monday, January 23	1	Noise Cancellation Techniques	Willy Sansen
	2	Noise Sampling in Switched Capacitor Filters	Willy Sansen
Tuesday, January 24	1&2	Noise Analysis in Continuous-Time and Sampled-Data Circuits	Christian Enz
Wednesday, January 25	1	Noise and Offset Reduction Techniques	Christian Enz
	2	Dynamic Offset-Cancellation Techniques	Kofi Makinwa
Thursday, January 26	1	Dynamic Offset-Cancellation Techniques	Kofi Makinwa
	2	Dynamic Element Matching Techniques	Kofi Makinwa
Friday, January 27	1	Dynamic Element Matching Techniques	Kofi Makinwa
	2	Case Studies in Precision Analog Circuit Design	Kofi Makinwa