



Techniques for Handling Noise and Variability in Analog Circuits

ON-LINE CLASS by Microsoft TEAMS

January 18-29, 2021

WEEK 1	JAN 18-22	10 Modules (1:30hr each), 2 Modules per day		
WEEK 2	JAN 25-29	9 Modules (1:30hr each), 2 Modules per day, except on Friday 1 module		
DAILY	Central European Time CET (Lausanne)	Eastern Standard Time EST (New York)	Pacific Standard Time PST (California)	India Standard Time IST (India)
Module 1	3:00-4:30 pm	9:00-10:30 am	6:00-7:30 am	7:30-9:00 pm
Module 2	5:00-6:30 pm	11:00 -12:30 pm	8:00-9:30 am	9:30-11:00pm
WEEK 1	Module			
Monday, January 18	1&2	Random Mismatch Origins		Marcel Pelgrom
Tuesday, January 19	1&2	Analyzing Mismatch and Yield in Analog Circuits		Marcel Pelgrom
Wednesday, January 20	1&2	Layout Strategies to Reduce Offset		Marcel Pelgrom
Thursday, January 21	1&2	Fundamentals of Noise in Electronic Devices		Christian Enz
Friday, January 22	1	Offset, CMRR and PSRR		Willy Sansen
	2	Variability in Bandgaps		Willy Sansen
WEEK 2	Module			
Monday, January 25	1	Noise Cancellation Techniques		Willy Sansen
	2	Noise Sampling in Switched Capacitor Filters		Willy Sansen
Tuesday, January 26	1&2	Noise Analysis in Continuous-Time and Sampled-Data Circuits		Christian Enz
Wednesday, January 27	1	Noise and Offset Reduction Techniques		Christian Enz
	2	Dynamic Offset-Cancellation Techniques		Kofi Makinwa
Thursday, January 28	1	Dynamic Offset-Cancellation Techniques		Kofi Makinwa
	2	Dynamic Element Matching Techniques		Kofi Makinwa
Friday, January 29	1	Dynamic Element Matching Techniques		Kofi Makinwa