



Operational Amplifiers: Theory and Design

ON-LINE CLASS

November 8-19, 2021

It is necessary to buy the book "Operational Amplifiers, Theory and Design", Third Edition, Johan Huijsing, 2017 Springer International Publishing, to follow the course.

WEEK 1	NOVEMBER 8-12			
WEEK 2	NOVEMBER 15-19			
	Central European Time	Eastern Standard Time	Pacific Standard Time	India Standard Time
	CET (Delft)	EST (New York)	PST (California)	IST (India)
DAILY SCHEDULE	3:00 - 6:30 pm	9:00 am - 12:30 pm	6:00 - 9:30 am	7:30 - 11:00 pm
WEEK 1				
November 8	3:00 - 4:00 pm	Introduction / Definitions/ Macromodels		J.F. Witte
	4:15 - 5:15 pm	Hands-on Simulations / Questions & Answers		R. Zamparette / J.F. Witte
November 9	3:00 - 4:00 pm	Input Stages: Offset, Noise, CMRR		R. Hogervorst
	4:15 - 5:15 pm	Rail-to-Rail Capability		R. Hogervorst
	5:30 - 6:30 pm	Hands-on Simulations / Questions & Answers		R. Zamparette / R. Hogervorst
November 10	3:00 - 4:00 pm	Output Stages: Voltage and Current Efficiency		K.J. de Langen
	4:15 - 5:15 pm	Class-AB Biasing		K.J. de Langen
	5:30 - 6:30 pm	Hands-on Simulations / Questions & Answers		R. Zamparette / K.J. de Langen
November 11	3:00 - 4:00 pm	Frequency Compensation, Slew-Rate		R.G.H. Eschauzier
	4:15 - 5:15 pm	Non-Linear Distortion		R.G.H. Eschauzier
	5:30 - 6:30 pm	Hands-on Simulations / Questions & Answers		R. Zamparette / R.G.H. Eschauzier
November 12	3:00 - 4:00 pm	Two-Stage Configurations		R. Hogervorst
	4:15 - 5:15 pm	Three-Stage Configurations		R. Hogervorst
	5:30 - 6:30 pm	Hands-on Simulations / Questions & Answers		R. Zamparette / R. Hogervorst
WEEK 2				
November 15	3:00 - 4:00 pm	Three-Stage Configurations		K.J. de Langen
	4:15 - 5:15 pm	Three-Stage Configurations		K.J. de Langen
	5:30 - 6:30 pm	Hands-on Simulations / Questions & Answers		R. Zamparette / K.J. de Langen
November 16	3:00 - 4:00 pm	Four-Stage Configurations		K.J. de Langen
	4:15 - 5:15 pm	Fully Differential OpAmps		K.J. de Langen
	5:30 - 6:30 pm	Hands-on Simulations / Questions & Answers		R. Zamparette / K.J. de Langen
November 17	3:00 - 4:00 pm	Dynamic Offset Compensation Techniques		J.F. Witte
	4:15 - 5:15 pm	Dynamic Offset Compensated Operational CMOS Amplifiers		J.F. Witte
	5:30 - 6:30 pm	Hands-on Simulations / Questions & Answers		R. Zamparette / J.F. Witte
November 18	3:00 - 4:00 pm	Auto-Zero and Chopper Amplifiers		J.F. Witte
	4:15 - 5:15 pm	Auto-Zero and Chopper Amplifiers Overview		J.F. Witte
	5:30 - 6:30 pm	Hands-on Simulations / Questions & Answers		R. Zamparette / J.F. Witte
November 19	3:00 - 4:00 pm	Capacitive - Coupled Amplifiers		Q. Fan
	4:15 - 4:45 pm	Capacitive - Coupled Amplifiers		Q. Fan
	5:00 - 6:00 pm	Evaluation		