



Low-Power Analog IC Design

ON-LINE CLASS on MS TEAMS

September 29 - October 10, 2025

WEEK 1		SEPT 29 - OCT 3			
WEEK 2		OCTOBER 6-10			
DAILY	Central European Time		Eastern Standard Time	Pacific Standard Time	India Standard Time
	CET (Lausanne)		EST (New York)	PST (California)	IST (India)
<i>Module 1</i>	3:00-4:30 pm		9:00-10:30 am	6:00-7:30 am	6:30-8:00 pm
<i>Module 2</i>	5:00-6:30 pm		11:00 am - 12:30 pm	8:00-9:30 am	8:30-10:00 pm
WEEK 1	<i>Module</i>				
DAY 1, Mon. September 29	1&2	MOS Transistor Modeling for Low-Voltage and Low-Power Circuit Design			Christian Enz
DAY 2, Tue. September 30	1&2	Design of Low-Power Analog Circuits using the Inversion Coefficient			Christian Enz
DAY 3, Wed. October 1	1&2	Noise Performance of Elementary Circuit Blocks			Boris Murmann
DAY 4, Thu. October 2	1&2	Opamp Topologies and Design Fundamentals			Boris Murmann
DAY 5, Fri. October 3	1	Low-Power High Efficiency OpAmp Design			Klaas Bult
	2	Low-Power High Efficiency Residue Amplifiers			Klaas Bult
WEEK 2	<i>Module</i>				
DAY 6, Mon. October 6	1	Analog Design Methodology and Practical Techniques for Frequency Compensation			Vadim Ivanov
	2	Energy Efficient Voltage References, Biasing in Analog Systems and Current Sources			Vadim Ivanov
DAY 7, Tue. October 7	1	Power Dissipation in ADC Building Blocks			Klaas Bult
	2	Power Dissipation in ADCs			Klaas Bult
DAY 8, Wed. October 8	1&2	Micropower ADCs			Kofi Makinwa
DAY 9, Thu. October 9	1&2	Energy Efficient Sensor Interfaces			Taekwang Jang
DAY 10, Fri. October 10	1	Low-Power Frequency Reference Circuits			Taekwang Jang
	2	Power Management With Nanoampere Consumption and Efficient Energy Harvesting			Vadim Ivanov