



Enabling Embedded Neural Network

ON-LINE CLASS on MS TEAMS

February 3-7, 2025

DAILY	Central European Time	Eastern Standard Time	Pacific Standard Time	India Standard Time
	CET (Lausanne)	EST (New York)	PST (California)	IST (India)
<i>Module 1</i>	3:00-4:30 pm	9:00-10:30 am	6:00-7:30 am	7:30-9:00 pm
<i>Module 2</i>	5:00-6:30 pm	11:00 am-12:30 pm	8:00-9:30 am	9:30-11:00 pm

	<i>Module</i>		
DAY 1, Mon February 3	1+2	Neural Network Introduction and Model Techniques	Tijmen Blankevoort, Meta
DAY 2, Tue February 4	1+2	Custom Hardware Accelerators and Scheduling Techniques	Marian Verhelst, KU Leuven & IMEC
DAY 3, Wed February 5	1+2	RISC-V and Multi-Core Architectures	Luca Benini, ETHZ/Uni Bologna
DAY 4, Thu February 6	1+2	Compiler Implications	Tobias Grosser, UC Cambridge
Day 5, Fri February 7	1+2	System Integration and Applications	David Aienza, EPFL