



Power Management

Live Course @ EPFL, Lausanne, Switzerland

AUGUST 25-29, 2025

Monday, August 25		
08:30 - 10:00 am	Fundamentals of SC Converters and Topologies	Filip Tavernier
10:30 - 12:00 am	Analysis and Modeling of SC Converters	Filip Tavernier
01:30 - 03:00 pm	Power Stages	Bernhard Wicht
03:30 - 05:00 pm	Gate Drivers	Bernhard Wicht
Tuesday, August 26		
08:30 - 10:00 am	GaN Drivers and Circuits	Bernhard Wicht
10:30 - 12:00 am	Protection and Sensing	Bernhard Wicht
01:30 - 03:00 pm	Fundamentals of Inductive DC-DC Converters	Bernhard Wicht
03:30 - 05:00 pm	Hybrid Converters	Bernhard Wicht
Wednesday, August 27		
08:30 - 10:00 am	Fundamentals of Linear Regulators	Pavan Hanumolu
10:30 - 12:00 am	LED Drivers Design	Pavan Hanumolu
01:30 - 03:00 pm	Digitally Controlled DC-DC Converters	Pavan Hanumolu
03:30 - 05:00 pm	Time-Based Control of DC-DC Converters	Pavan Hanumolu
Thursday, August 28		
08:30 - 10:00 am	Interference and PSRR	Michiel Steyaert
10:30 - 12:00 am	Bandgap Voltage References	Michiel Steyaert
01:30 - 05:00 pm	DC-DC: From Discrete To Fully CMOS Integrated	Michiel Steyaert
Friday, August 29		
08:30 - 10:00 am	Practical Techniques of Frequency Compensation	Vadim Ivanov
10:30 - 12:00 am	Design of LDO's with Instant Load Regulation & Unconditional Stability	Vadim Ivanov
01:30 - 03:00 pm	Circuit Techniques for Integrated Switching Regulation	Vadim Ivanov
03:30 - 05:00 pm	Nanopower Design Techniques and Efficient Energy Harvesting	Vadim Ivanov