LOW-POWER ANALOG CIRCUIT DESIGN

Live Course @ EPFL, Lausanne, Switzerland

JUNE 17-21, 2024

Monday, June 17		
08:30 - 12:00 am	MOS Transistor Modeling for Low-Voltage and Low-Power Circuit Design	Christian Enz
01:30 - 05:00 pm	Design of Low-Power Analog Circuits using the Inversion Coefficient	Christian Enz
Tuesday, June 18		
08:30 - 12:00 am	Noise Performance of Elementary Circuit Blocks	Boris Murmann
01:30 - 05:00 pm	Opamp Topologies and Design Fundamentals	Boris Murmann
Wednesday, June 19		
08:30 - 10:00 am	Low-Power High Efficiency OpAmp Design	Klaas Bult
10:30 - 12:00 am	Low-Power High Efficiency Residue Amplifiers	Klaas Bult
01:30 - 03:00 pm	Analog Design Methodology and Practical Techniques for Frequency Compensation	Vadim Ivanov
03:30 - 05:00 pm	Energy Efficient Voltage References, Biasing in Analog Systems and Current Sources	Vadim Ivanov
Thursday, June 20		
08:30 - 10:00 am	Power Dissipation in ADC Buidling Blocks	Klaas Bult
10:30 - 12:00 am	Power Dissipation in ADCs	Klaas Bult
01:30 - 05:00 pm	Micropower ADCs	Kofi Makinwa
Friday, June 21		
08:30 - 12:00 am	Energy Efficient Sensor Interfaces	Taekwang Jang
01:30 - 03:00 pm	Low-Power Frequency Reference Circuits	Taekwang Jang
03:30 - 05:00 pm	Power Management With Nanoampere Consumption and Efficient Energy Harvesting	Vadim Ivanov