



## Advanced Analog Circuit Design

Live Course @ EPFL, Lausanne, Switzerland

**JUNE 23-27, 2025**

<b>Monday, June 23</b>		
08:30 - 12:00 am	Time Assisted Analog Design	Pavan Hanumolu
01:30 - 05:00 pm	Analog Building Blocks	Kofi Makinwa
<b>Tuesday, June 24</b>		
08:30 - 12:00 am	Stability of Operational Amplifiers	Kofi Makinwa
1:30 - 3:00 pm	Noise	Kofi Makinwa
03:30 - 05:00 pm	Offset and CMRR Limitations	Kofi Makinwa
<b>Wednesday, June 25</b>		
08:30 - 12:00 am	Offset and 1/f Noise Reduction Techniques	Kofi Makinwa
01:30 - 05:00 pm	CMOS Switched-Capacitor Circuit Design	Christian Enz
<b>Thursday, June 26</b>		
08:30 - 10:00 am	Practical Techniques of Frequency Compensation	Vadim Ivanov
10:30 - 12:00 am	Bandgap Voltage References	Vadim Ivanov
01:30 - 05:00 pm	Techniques for OpAmp Speed and Accuracy Improvement	Vadim Ivanov
<b>Friday, June 27</b>		
08:30 - 10:00 am	Gain Boosting & How to Judge OpAmp Settling Behaviour	Klaas Bult
10:30 - 12:00 am	Design Mistakes You'd Rather Not Talk About	Klaas Bult
01:30 - 05:00 pm	Continuous-Time Filters	Christian Enz