ON-LINE CLASS on MS TEAMS

Introduction to Analog Circuit Design

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November 16-18 and 23-25, 2026

DAILY	Central European Time	Eastern Standard Time	Pacific Standard Time	India Standard Time
	CET (Lausanne)	**EST (New York)**	**PST (California)**	**IST (India)**
Module 1	3:00-4:30 pm	9:00-10:30 am	6:00-7:30 am	7:30-9:00 pm
Module 2	5:00-6:30 pm	11:00 am-12:30 pm	8:00-9:30 am	9:30-11:00 pm

	Module	
DAY1, Monday, Nov. 16	1	**Introduction –** Types of Analysis and Introduction to OpAmps & Feedback. Time-Domain & Stability.
	2	**MOS-Transistor –** The MOS-Transistor – Principle. Week Inversion.
DAY 2, Tuesday, Nov. 17	1	**MOS-Transistor –** Second-Order Effects
	2	**Making Gain –** How to make Gain? DC & AC Behavior. Transient Behavior & Fundamental Limitations. How to Make Gain? Cascoding! **HW0 – Introduction to Homeworks**
DAY3, Wednesday, Nov. 18	1	**Gain-Boosting –** How to make even more Gain ? Gain-Boosting! Settling Behavior & Handling Doublets. Judging Settling Behavior.
	2	**Noise –** Noise Origins and Basic Properties. **HW1 – Interview Questions!**
DAY 4, Monday, Nov. 23	1	**Noise –** How to Handle Noise in Circuit Analysis? Noise in Circuit Design.
	2	**Distortion –** Distortion Analysis of Slightly Nonlinear Circuits. Distortion and Feedback. Example of Distortion Calculation. **HW2 – Settling Accuracy**
Day 5, Tuesday, Nov. 24	1	**Distortion –** Example with Numbers. Advanced Topics in Distortion. Distortion in Multi-Stage Designs.
	2	**Basic Building Blocs –** Basic Sub-Circuits: Sourse Follower, Differential Pairs, Current Sources & Biasing. **HW3 – Noise**
Day 6, Wednesday, Nov. 25	1	**Architectures –** Single-Ended OpAmp Architectures. Fully Differential OpAmp Architectures. OpAmp Design for Certain Specifications.
	2	**Two-Stage Design –** 2-Stage OpAmps: Compensation, Pole Splitting, Transfer-Function & Time Constants, Stability Limits & The Nulling Resistor, Summary. **HW4 – Distortion**