



INTRODUCTION TO ANALOG DESIGN

ON-LINE CLASS on MS TEAMS

October 30 - November 3, 2023

DAILY SCHEDULE		Central European Time	Eastern Standard Time	Pacific Standard Time	India Standard Time
		CET (Lausanne)	EST (New York)	PST (California)	IST (India)
Module 1		3:00 - 4:30 pm	9:00 am - 10:30 am	6:00 - 7:30 am	7:30 - 9:00 pm
Module 2		5:00 - 6:30 pm	11:00 am - 12:30 pm	8:00 - 9:30 am	9:30 - 11:00 pm
DAY	<i>Module</i>	LEVEL 1			
1	Monday, October 30	1	Basic Elements		
		2	MOSFET Large Signal Modelling		
2	Monday, October 31	1	MOSFET Small Signal Modelling		
		2	LTSpice Simulation		
3	Tuesday, November 1	1	Gain Circuits		
		2	Current Mirrors		
4	Wednesday, November 2	1	Diff Amps A		
		2	Diff Amps B		
5	Friday, November 3	1	Frequency Response A		
		2	Frequency Response B		

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